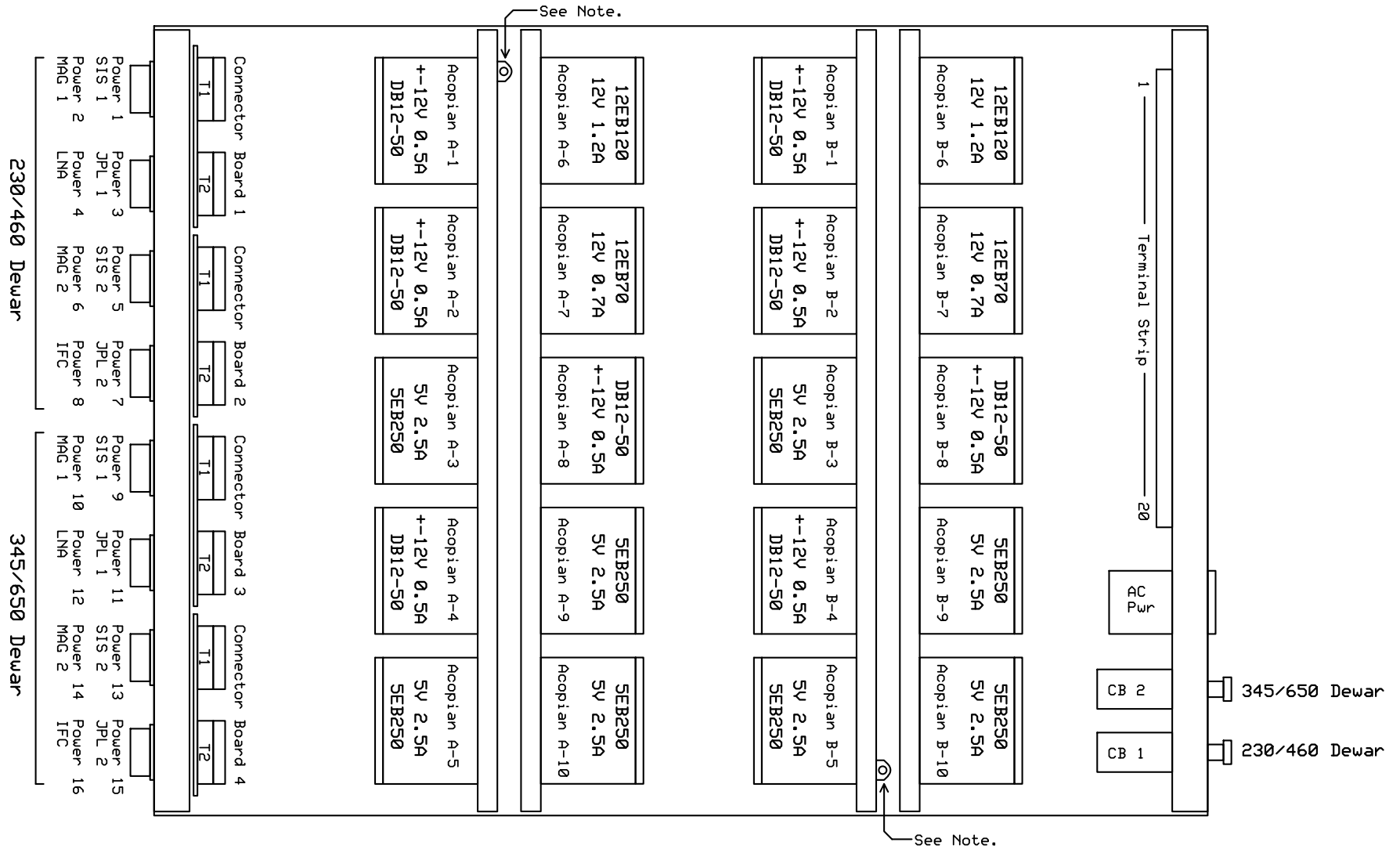


# Component Layout and Identification



## Note:

Solder lug positioned under Acopian top mounting screw at locations shown to provide a chassis connection.

## Change from Rev 2.0:

Added trim resistors and wiring to boost voltage to JPL PA supplies

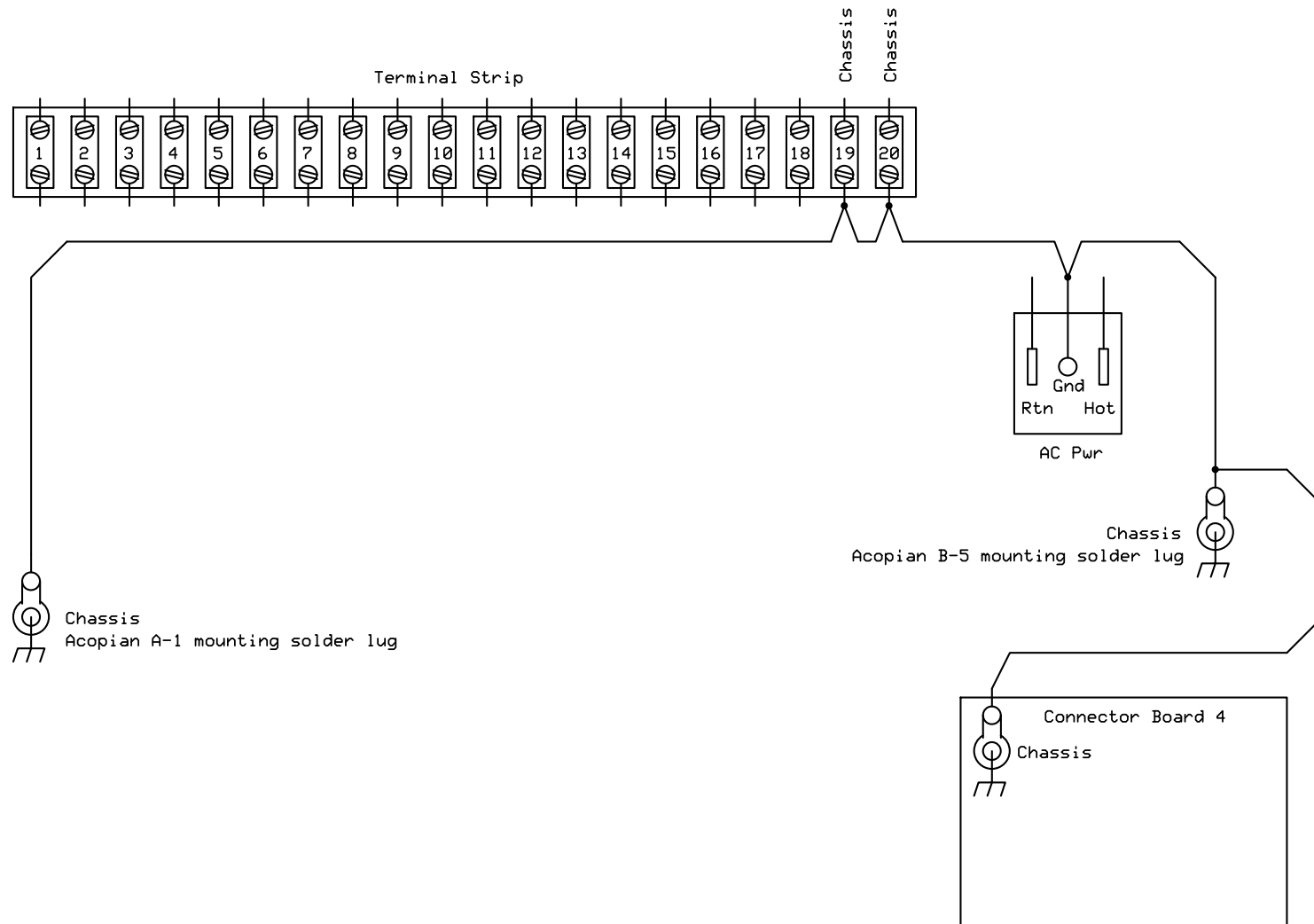
CS0 Electronics Caltech  
System Power Supply

F. Rice

Rev 2.1  
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# Chassis Grounding



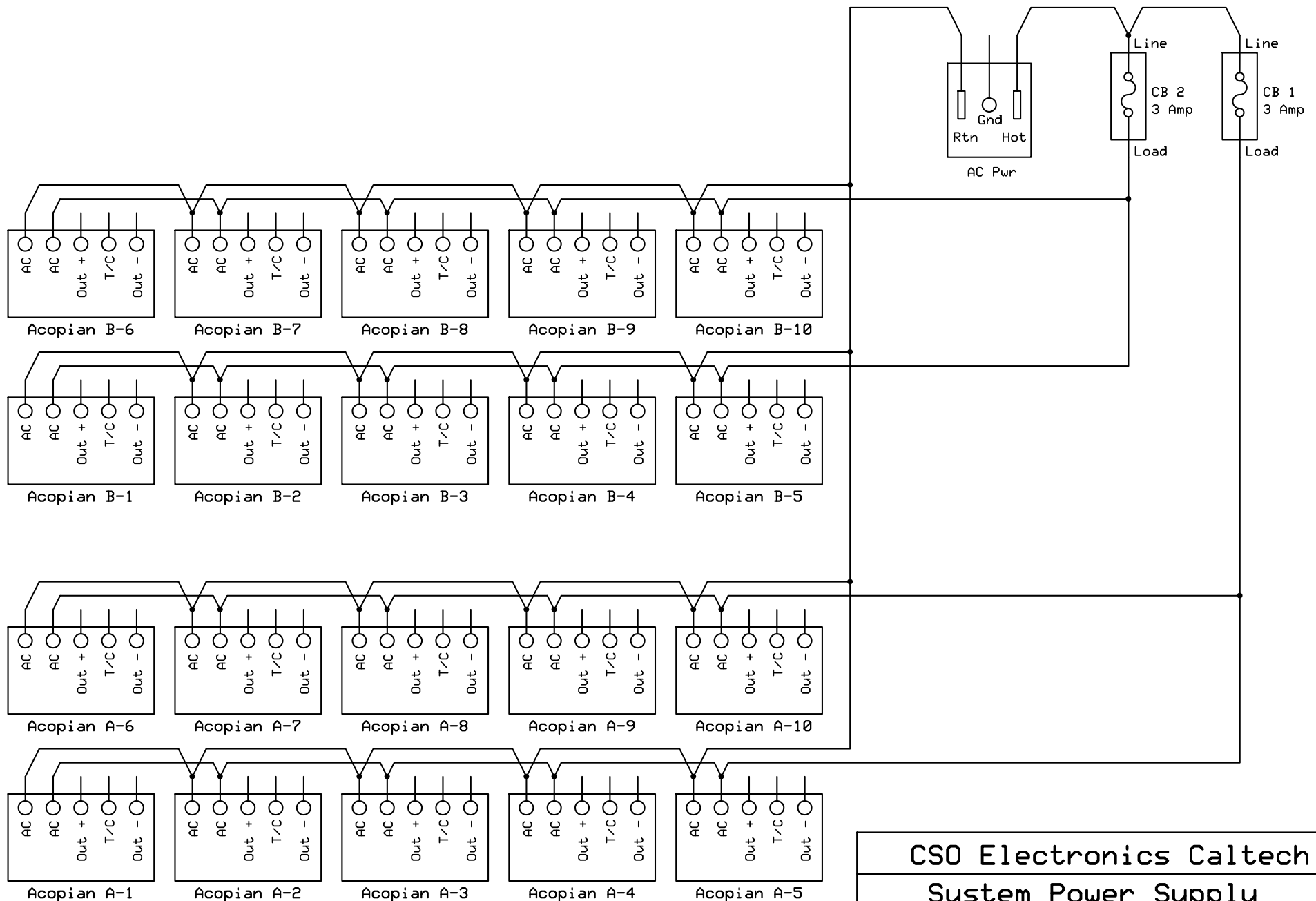
CSO Electronics Caltech  
System Power Supply

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# AC Power Wiring



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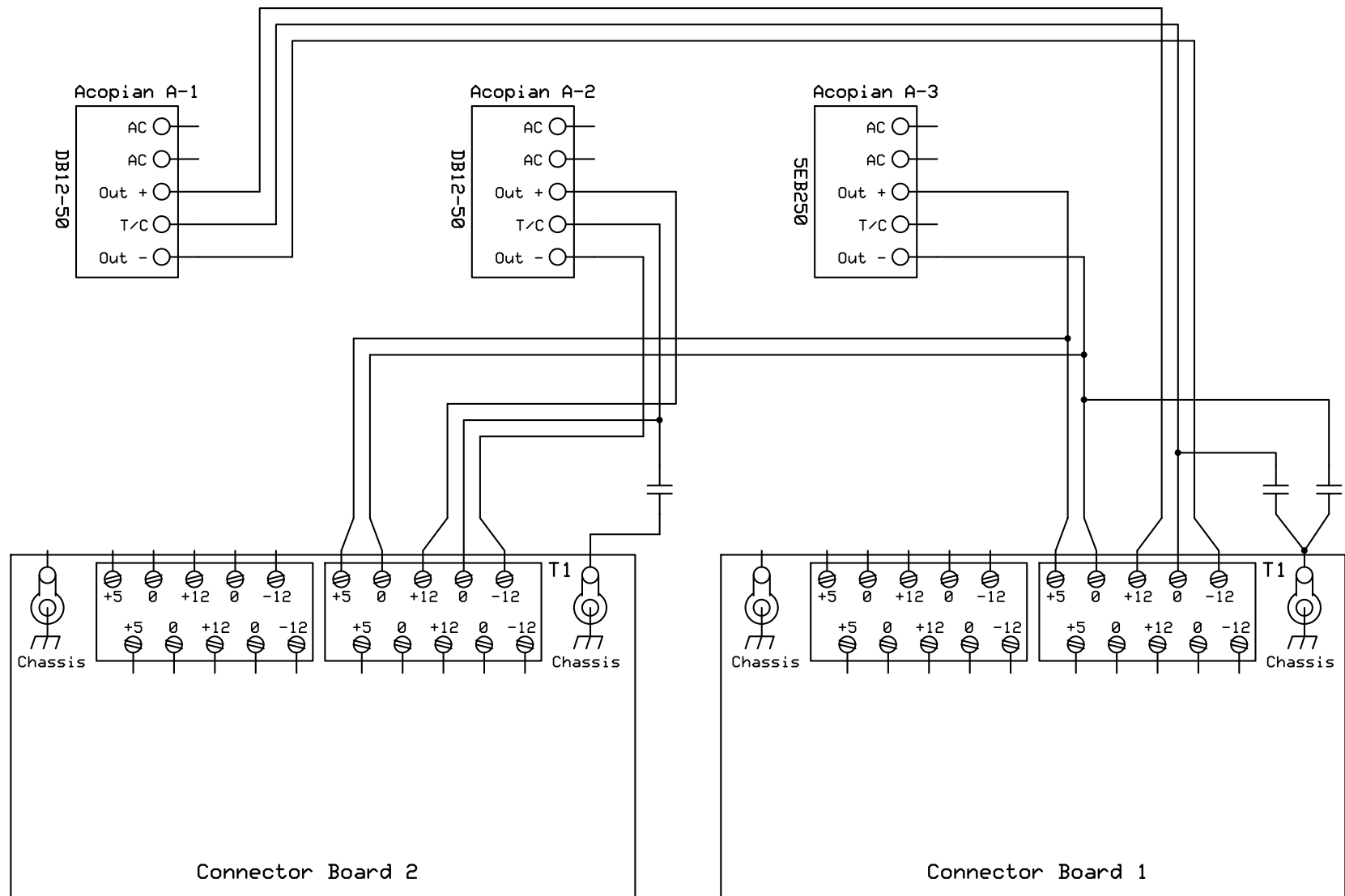
F. Rice

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# SIS - 230/460 Dewar



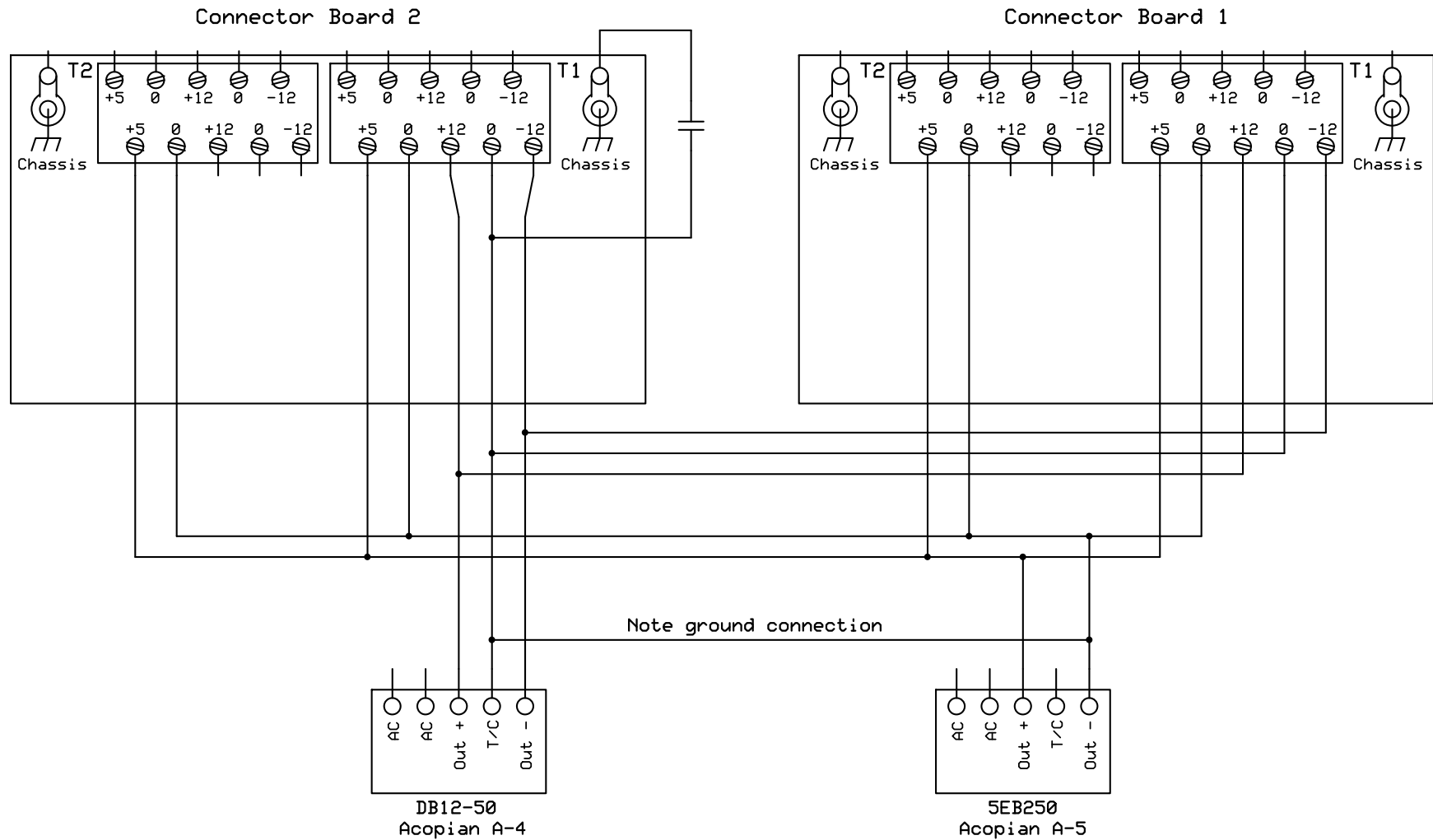
CSO Electronics Caltech  
System Power Supply

F. Rice

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3/19/2012

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# Magnets - 230/460 Dewar



CSO Electronics Caltech  
System Power Supply

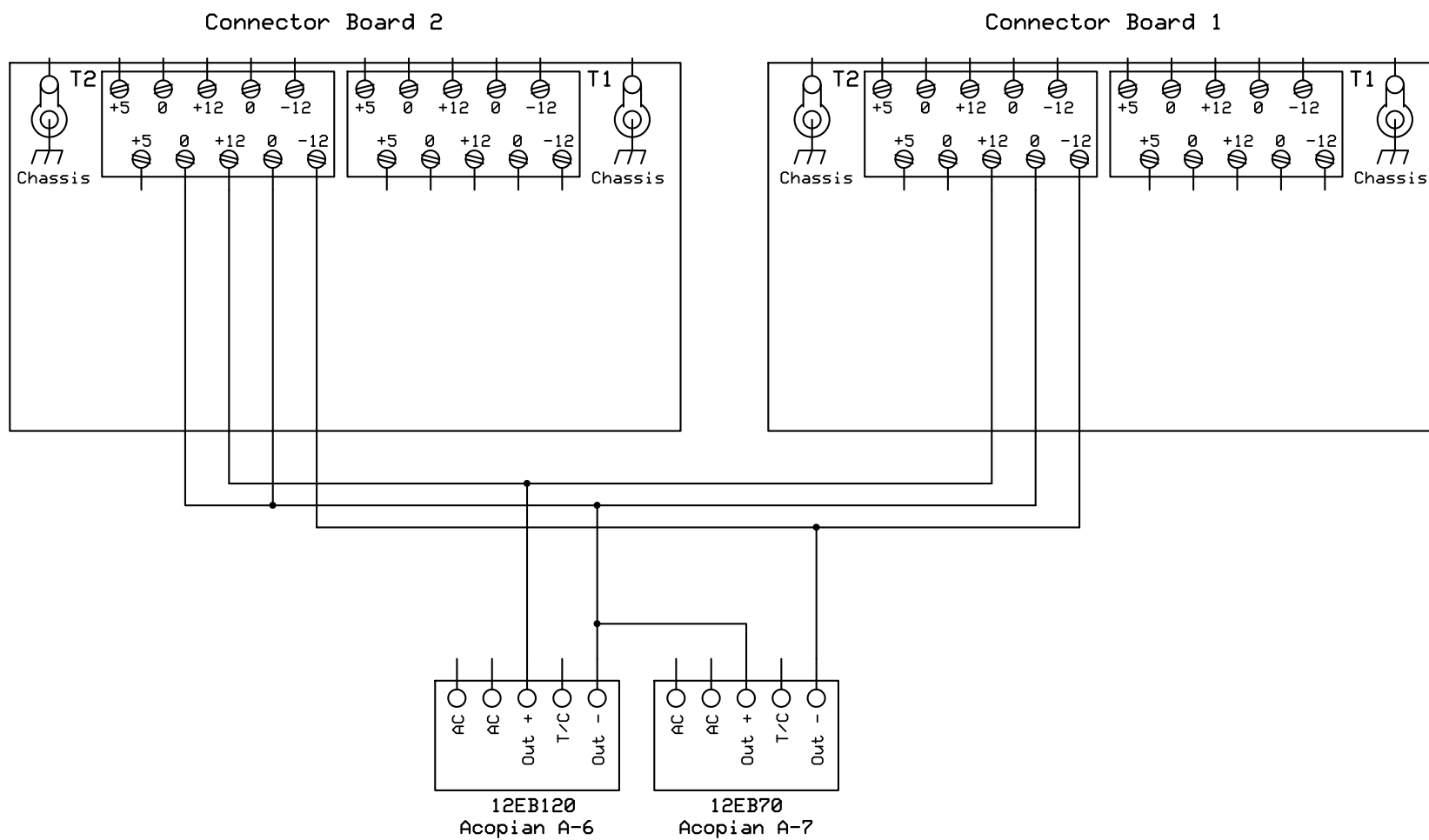
F. Rice

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3/19/2012

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# LNA and Warm IF - 230/460 Dewar



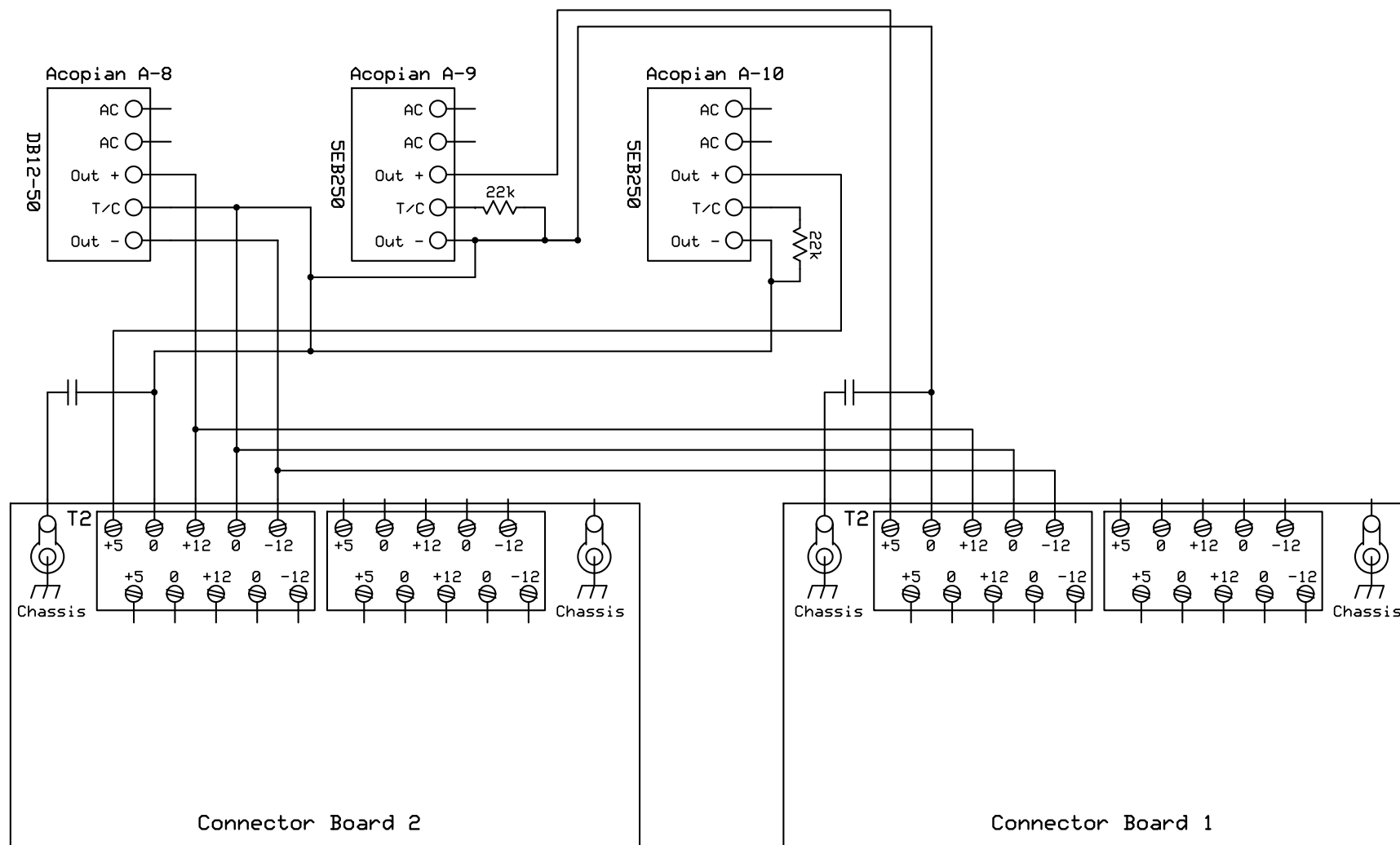
CSO Electronics Caltech  
System Power Supply

F. Rice

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3/19/2012

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# JPL LO Amps - 230/460 Dewar



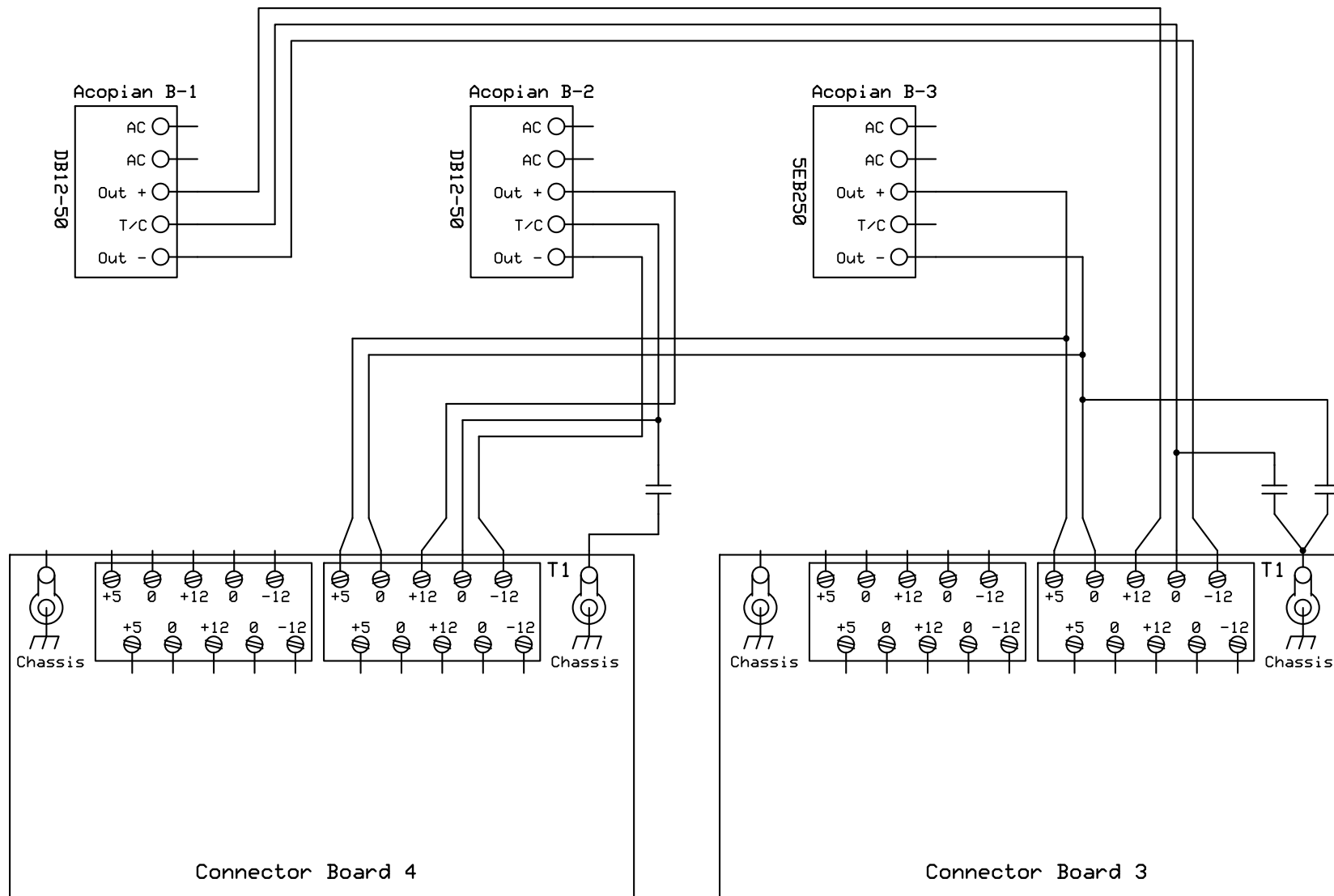
CSO Electronics Caltech  
System Power Supply

F. Rice

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3/19/2012

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# SIS - 345/650 Dewar



CSO Electronics Caltech  
System Power Supply

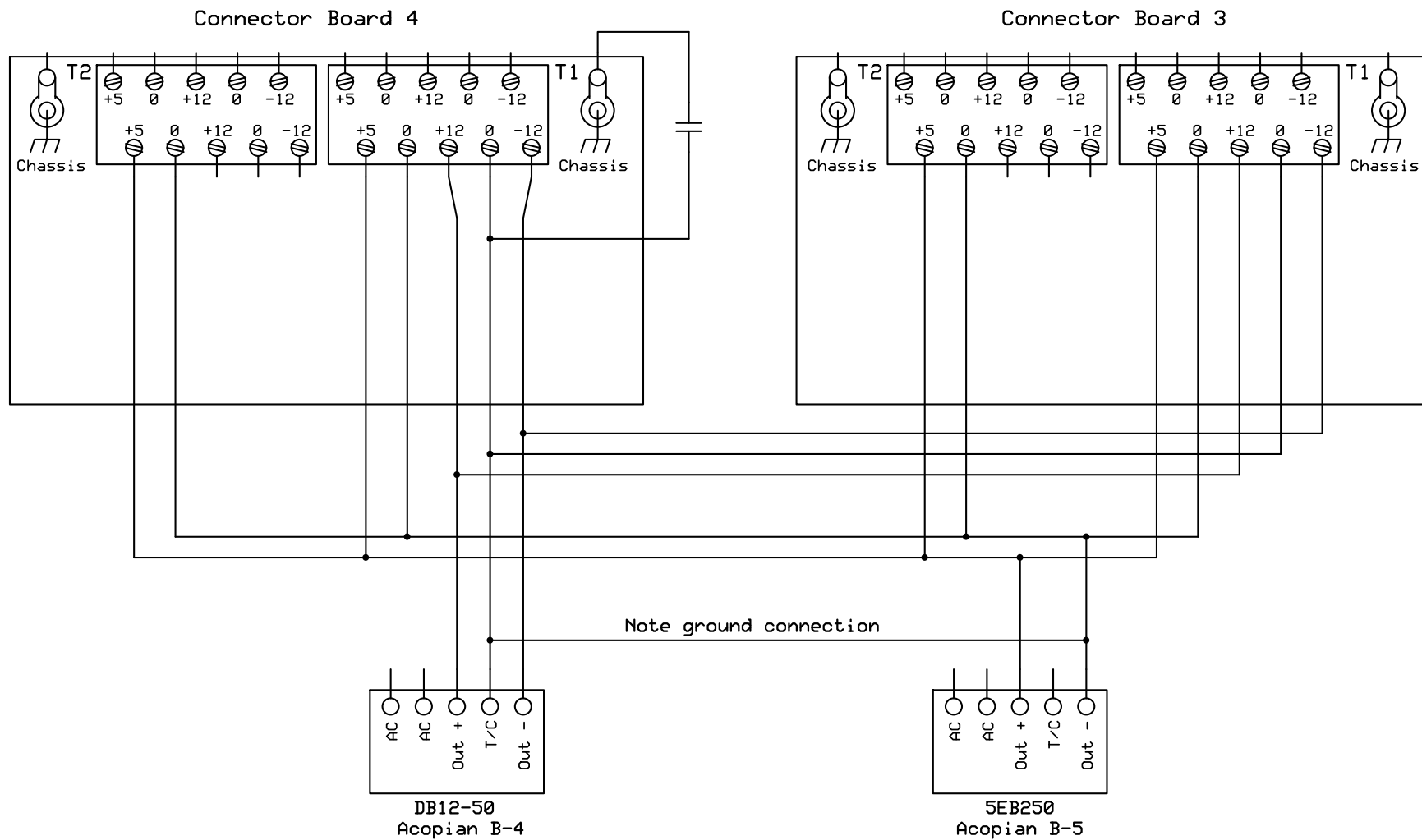
F. Rice

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3/19/2012

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# Magnets - 345/650 Dewar



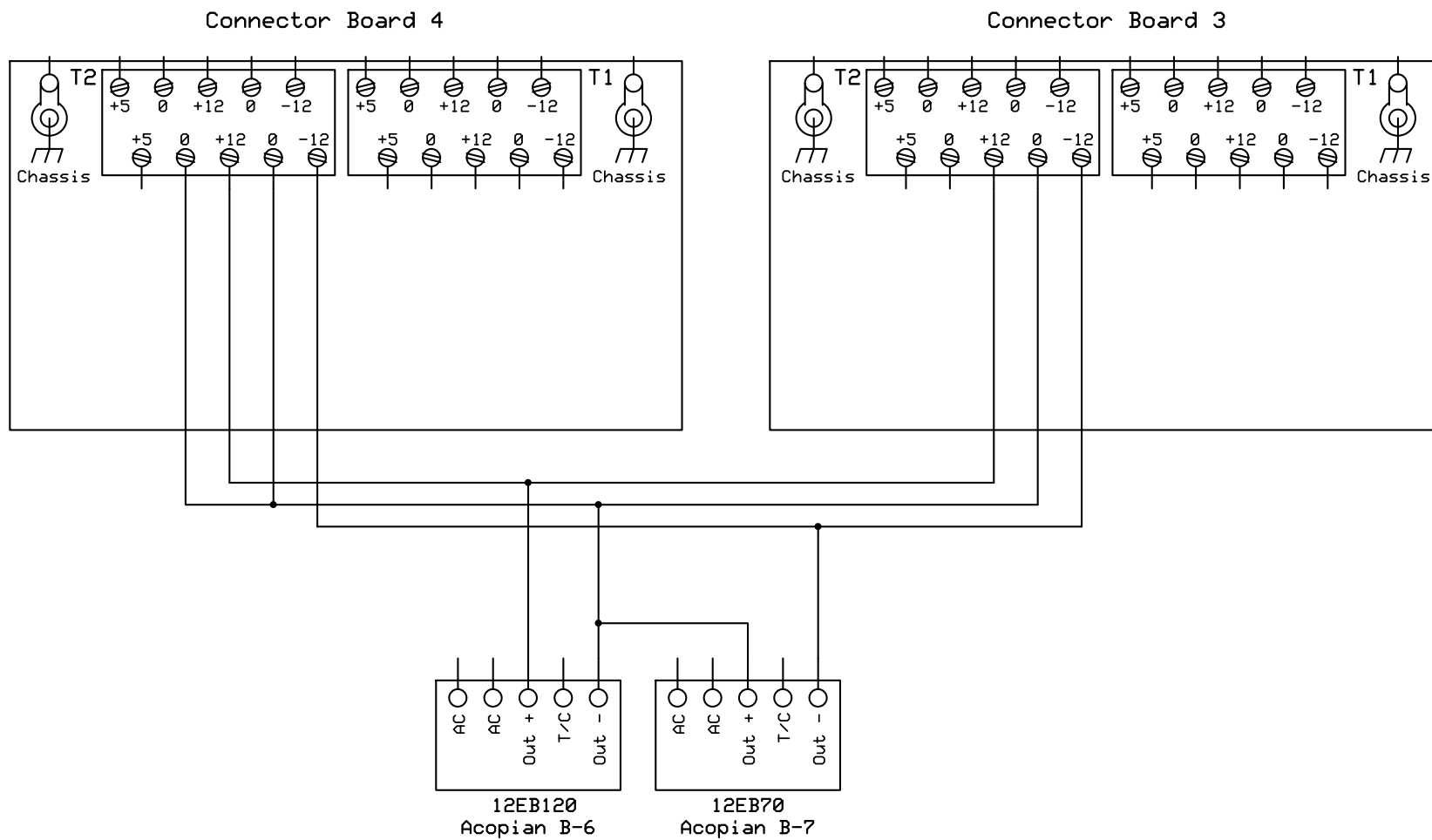
CSO Electronics Caltech  
System Power Supply

F. Rice

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# LNA and Warm IF - 345/650 Dewar



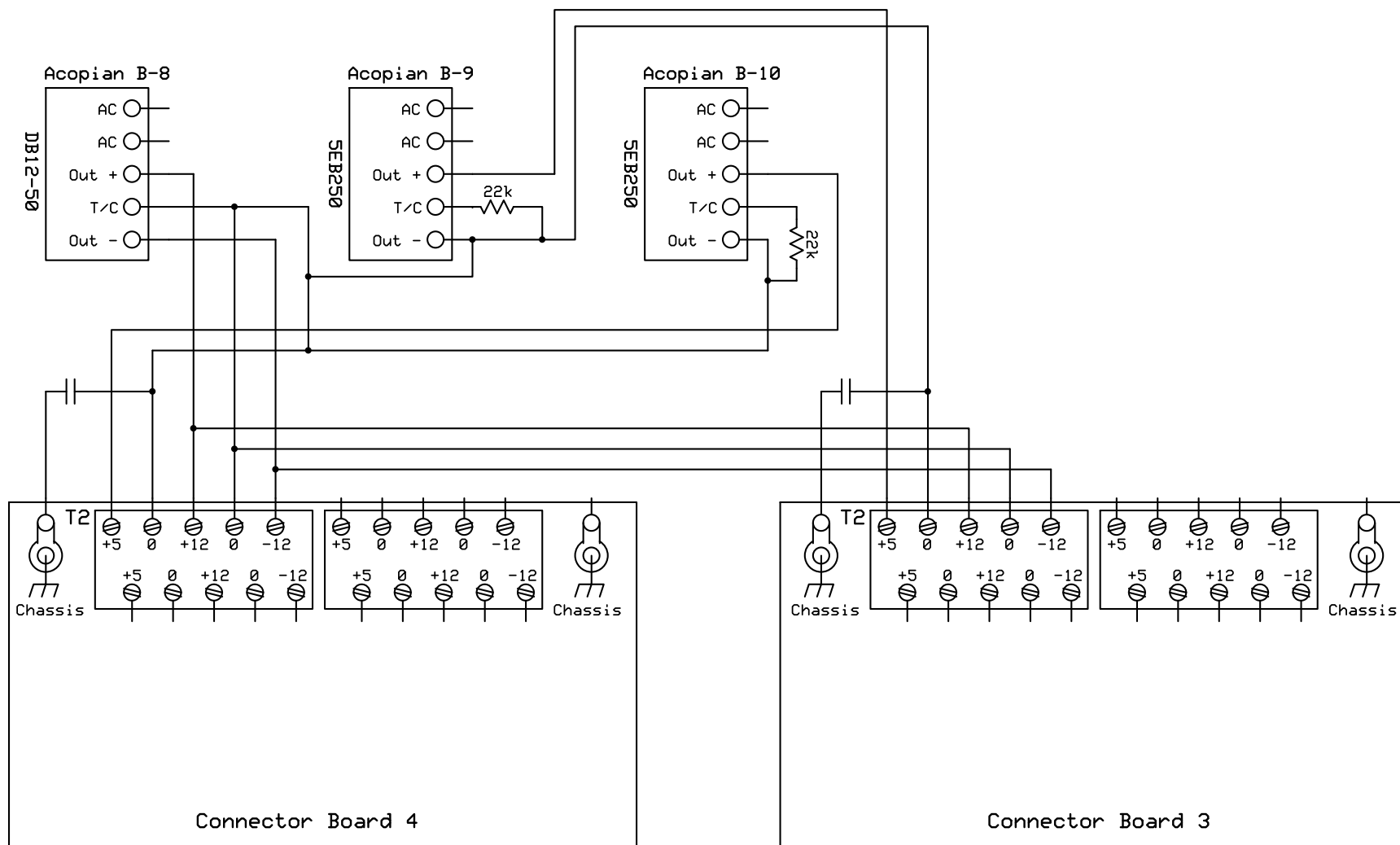
CSO Electronics Caltech  
System Power Supply

F. Rice

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# JPL LO Amps - 345/650 Dewar



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3/19/2012

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